

PCB Testing Protocol/Best Practices

1 Introduction

In order to achieve comparable testing results from site to site, a fully defined test protocol is required. The goal of this test protocol is to specify important test parameters that, when set up similarly in different machines, will allow those machines to capture similar results. While the following recommendations are a good starting point, different samples will require different setups to optimize the captured data. Where different, users should communicate exact test conditions.

2 Hardware

An Akrometrix TherMoiré AXP or PS400 w/ Studio Upgrade is recommended for optimal surface characterization. A 100 line per inch (LPI) grating along with a zoom lens is also recommended. Special attention should be paid to what field of view is chosen for each test. Because zoom lenses have no detents, it is especially important to use the same size ROI between systems. The users should set an ROI size in pixels and match the edges of the ROI to the part edges using the lens zoom. Certain applications benefit from different fields of view and gratings. When differing from the standard hardware, all parties should coordinate such that they are using the same setup.

3 Calibration (Fringe Value / Fringe Height)

Before measurement, each machine should be calibrated to ensure system accuracy. Calibration should be performed according to each equipment manual and use a recently calibrated standard. It is recommended that calibration blocks be recertified yearly by Akrometrix. The calibration procedure calculates a unique constant, the **Fringe Value**, which ensures that the system accuracy will be the greater of 2.5 μ m or 3% of the measured value.

If there are periodic ripples visible in each room-temperature 3D result, which have double the frequency as that in the phase image (see **Figure 1**), make sure the sample is not touching the grating during phase shifting. If the sample has not touched the grating and this problem remains, another constant, the **Fringe Height**, may need to be calibrated. Contact Akrometrix for a detailed procedure. If no ripples are visible, the fringe height is properly calibrated.



Figure 1 Phase Image with resulting 3D Surface showing periodic ripples



4 Sample Characteristics

There are several sample and sample prep characteristics that are recommended for use with Akrometrix TherMoiré systems. Firstly, the measurement technique used in all TherMoiré systems is ideally suited to measuring surfaces that are continuous in nature. That is, they do not have areas of interest that contain step heights. An example of this ideal type of surface is a typical bare PCB. If that PCB has components mounted on it, then it is an example of a less than ideal surface. In this case, the PCB can be measured, but all the components would not be able to be related to each other and/or the underlying PCB.

In addition to the continuous criteria, the ideal surface finish and color for use with the shadow moiré technique is one that is diffuse and white. Such a sample surface will result in the least amount of noise and highest contrast for the moiré fringe pattern. In order to achieve this surface type, a thin layer of high-heat white paint should be applied on top of the sample. This coating should be scattered uniformly across the whole sample but still be semi-transparent so that surface features are not completely covered (see **Figure 2**). Akrometrix uses and recommends Rust-Oleum[™] Specialty High Heat white enamel spray paint.

Unfortunately, a painted surface is not very conducive to tape adhesion, especially at elevated temperatures. As such, if thermocouple measurements are desired on the painted surface, a small area should be left unpainted for attaching a thermocouple with Kapton® tape as shown in **Figure 2**.

Finally, in addition to the above characteristics, it is important to take moisture sensitivity into consideration. JEDEC standard J-STD-020D.1 regarding moisture sensitivity levels is widely adhered to in the electronics industry. This same standard should be followed while testing samples with Akrometrix equipment. Typically, the main consideration is that samples are baked prior to exposure to reflow cycles. This baking should occur after any paint has been applied to the sample.



Figure 2 Painted sample on UAS Fixture



5 Sample Placement

Sample placement is another critical area which should be taken into consideration. The most important thing to pay attention to is consistency with sample support and placement between sites. However, replicating production reflow conditions is also an important consideration. Ideally, samples should be supported however they are in production reflow ovens. This is of particular importance for large area PCBs where gravity induced deflection can be quite significant, especially above glass transition temperatures.

In production environments where boards are supported at their edges, the standard rail supports present in any Akrometrix TherMoiré system will suffice. For cases where a belt supported reflow oven is used, a Uniform Area Support (UAS) fixture is better suited to replicating production reflow conditions (see **Figure 2**). This fixture is designed to minimize gravity induced deflection while maintaining minimal surface contact points and allowing IR energy transmittance. While using this fixture, it is advisable to limit use of the system blower, as the resultant air currents can result in excessive vibration of the sample and cause it to "walk" out of the FOV. This is especially true for lighter samples.

Another important consideration is sample orientation. Akrometrix recommends that samples be oriented such that the longer edge is aligned from left to right as the user faces the system. As a general rule, if there is only one lettering orientation on a board, the PCB should be oriented such that these markings are readable as the user faces the system (see **Figure 3**). More specifically, if the same type of board is being measured at multiple sites and it does not contain only one lettering orientation, the test operators should communicate what orientation they have placed the board in. Everyone should follow this rule so that the same coordinate system can be established during data analysis.



Figure 3 Thermocouple Placement and Sample Orientation



6 Thermocouple Placement

In general, it is best to place the driving thermocouple on bottom center of the sample. This area will receive the most IR energy and highest heating rates. All other points will typically be at the same temperature or lower during heating. If other points on the sample surface are of interest, up to 8 thermocouples can be placed on the sample at one time. Akrometrix does not recommend placing thermocouples on the top (measured) surface as their attachment areas will have to be digitally removed (masked) from the phase images.

Thermocouples should be attached to the sample surface using thermal grease and Kapton tape® as described in JEDEC standard JESD22B112. Alternatively, thermocouples can be more permanently attached using some sort of high temperature thermally conductive epoxy. **Figure 3** presents an example of thermocouple attachment on the sample surface.

7 Data Acquisition

- **Grating Cleanliness:** Because many FR-4 type materials tend to outgas at elevated temperatures, it is important that the grating be cleaned between thermal runs. Lint-free paper towels or cloths can be used along with any commercially available glass cleaner or IPA to remove these residues. The user should only wipe with the direction of the grating in order to minimize potential for grating damage. In addition, it is advised to clean any out-gassing residues as soon after the thermal cycle, during which they were deposited, as possible, so as to make their removal easier. These residues become more difficult to remove as they are "baked" on during subsequent thermal cycles.
- Light Levels: Set both light sources to approximately 90 percent (Akrometrix finds this to be a good compromise between light intensity and bulb life) of their maximum value and adjust the lens iris to achieve proper fringe contrast. Make sure the image is not saturated (bright spots with constant maximum grayscale values) or too dark (there is still a lot of room to increase the brightness before saturation). For optimal fringe contrast it is best to have the image brighter without any light saturation. The Akrometrix Studio software is capable of showing saturated light and dark pixels on the image, so that light saturation can be avoided. The Studio software also tracks an electronic iris setting (0-100%); however, the lens will still have a manual adjust that needs to be replicated between different systems.
- Sample to Grating Distance: Maintain a reasonable distance between the sample and grating. Too close or too far can result in loss of data or inaccurate results. For consistency Akrometrix recommends jogging the sample up until it touches the grating, and then lowering it by approximately 200 mils or 5000 µm when using the 100 LPI grating. On samples that have been coated as in Figure 2, this distance allows good fringe contrast while leaving space for sample deflection towards the grating. This also reduces the effect of out-gassing during the thermal cycle. For uncoated samples a shorter working distance may need to be selected. For assembled PCBs with taller components, a longer working



distance may need to be used to avoid sample to grating contact. Again, consistency between systems is the key.

- Phase Amplitude Threshold: Phase amplitude threshold can be used in one of two ways. The first method is to use phase amplitude threshold to mask noisier regions of an ROI. For this case recommended phase amplitude threshold values are 1, 2, or 3. For samples that exhibit excessive out-gassing during thermal cycles, lower phase amplitude threshold values should be used to keep more data. Phase amplitude threshold can also be used to mask around complex board shapes. Recommended values for this case range from 5-20. Higher values introduce the possibility of losing good data on the sample surface, while lower values may not effectively mask the areas without any board surface. Akrometrix Studio tracks the phase amplitude threshold used on any given phase image. Different samples may need to use different phase amplitude thresholds, but the same form factors should always be tested with the same phase amplitude thresholds.
- **Regions of Interest (ROI):** The ideal approach for ROI selection is to size the ROI 2-3 pixels from the edge of the PCB. This allows a nearly complete data set, while not creating any "edge effects" from pixels off the edge of a sample. This ROI choice will decrease the need for later phase image processing. However, in some cases, the sample will move laterally inside the heating chamber. In these cases ROI selection can be selected well outside the dimensions of the part and cropped more carefully in post processing.

8 Data Analysis

- Akrometrix Studio: Of all the feature changes and improvements in the Akrometrix Studio software, the data analysis software (Surface Analysis) provides the largest technical advances to the TherMoiré systems. When at all possible data analysis should be performed on this new platform.
- **Units:** Z-displacement units are most commonly shown in microns.
- **Reference Plane:** LSF rotation must be applied during analysis to obtain correct coplanarity values. Choosing LSF rotation also allows concave or convex shape to be defined as in JEDEC standard JESD22B112 or JEITA standard ED-7306.
- **Gauges:** A number of gauges are available to numerically represent a given data set, with a number of added options in Studio. Coplanarity is defined as the vertical displacement difference between the highest and lowest pixel in an entire data set. While this gauge can be noise sensitive, it is the most commonly used gauge for data comparison. Additionally, signed coplanarity can be used to show the direction of warpage.
- Phase filtering (smooth): To reduce noise in an image, phase filtering can be applied once to each phase image. For boards showing very high levels of warpage the phase filtering option should not be utilized. Phase filtering will also typically change calculated gauge values; for this reason all images for a specific sample ROI or no images should be phase filtered.
- **ROI (Cropping/Partitioning):** If the global ROI was made larger than the sample, then phase images should be cropped 2-3 pixels inside the visible edge



of the sample. Akrometrix Studio allows for more accurate cropping using the "view surface image" capability. Local areas on the PCB can also be partitioned for more detailed analysis of these areas. In all cases, if lens zoom is consistent, pixel dimensions for all ROIs should be the same.

- Masking: If there are noise or spikes in the results, they should be masked out before further analysis. This noise will substantially affect the coplanarity value since it relies on only two values among millions of data points. Also, depending on the use of phase amplitude threshold, areas without any PCB surface will need to be masked for accurate analysis. Phase amplitude threshold filtering may already have removed these areas. Very small features can often be ignored if they do not cause noise in the extrema of the data set. Finally, any step heights or discontinuity in the measured surface will also need to be masked. This particularly applies to assembled PCBs. Component surfaces can often be measured as local ROIs; however, the height between taller component surfaces and the PCB surface cannot be correlated with the shadow moiré technique. Akrometrix Studio gives much stronger masking capabilities, particularly when dealing with complex shapes or fully assembled boards.
- Unwrapping Errors: An unwrapping error occurs when the software cannot count between the beginning and end of a fringe. Noisier areas or step heights on the sample can cause these errors. Unwrapping errors will appear as a sudden, one fringe value height change that moves diagonally away from the source of the error. Results should never contain unwrapping errors, as these areas contain false data. Unwrapping errors are most commonly solved by masking the problem areas that are causing the incorrect fringe counting. An example unwrapping error is shown in Figure 4(a). In Figure 4(b) the error is corrected by masking the area where the unwrapping error begins.





- **Scale:** A common Z-scale should be chosen for all tests in a single form factor. The range of this scale will vary per sample.
- **Graphical Output:** 3D Surface plots are the preferred graphical representation of PCB shapes.



• **Relative plots (Optional):** Relative data analysis is not standard to the average test, but should be mentioned here. This capability allows two data sets of the same size to be subtracted from one another. This subtraction gives a relative warpage change, most commonly used for the change of a single part over temperature.

9 Reporting

Akrometrix provides software for organizing graphical results into a report format. As a baseline this report should include 3D Surface plots, color legends for the Surface plots, and the coplanarity value for each plot. Addition information in the form of graphical plots and numerical gauges can be added to the report as needed. Other available graphical outputs include Color Contour plots and point to point 2D chord plots. Available gauges are numerous, particularly in Studio, and can include bow, twist, signed warpage, chord maximum, and chord minimum. Signed Warpage is implemented in Akrometrix software according to JEITA standard ED-7306. An example Akrometrix report output is shown below in **Figure 5**.





Figure 5 Akrometrix Report with 3D Surface Plots